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Date: May 8, 2002

By: *Sandy Reisman*
Sandy Reisman

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Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

STEPHEN E. JONES

APPLICATION No.: 09/930,106

FILED: AUGUST 14, 2001

FOR: METHOD FOR EXECUTING A 32-BIT
FLAT ADDRESS PROGRAM DURING
A SYSTEM MANAGEMENT MODE
INTERRUPT

CONFIRMATION No. 9880

ART UNIT: 2131

Information Disclosure Statement Within Three Months of Application Filing or Before First Action – 37 CFR 1.97(b)

Commissioner for Patents
Washington, D.C. 20231

Sir:

1. Timing of Submission

This information disclosure is being filed within three months of the filing date of this application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever occurs last [37 CFR 1.97(b)]. The references listed on the enclosed Form PTO/SB/08A (modified) may be material to the examination of this application; the Examiner is requested to make them of record in the application.

2. Cited Information

Copies of the following references are enclosed:

- All cited references
- References marked by asterisks
- The following:

Copies of the following references can be found in parent application Ser. No.:

- All cited references
- References marked by asterisks
- The following:

The following references are not in English. For each such reference, the undersigned has enclosed (i) a translation of the reference; (ii) a copy of a communication from a foreign patent office or International Searching Authority citing the reference, (iii) a copy of a reference which appears to be an English-language counterpart, or (iv) an English-language abstract for the reference prepared by a third party. Applicant has not verified that the translation, English-language counterpart or third-party abstract is an accurate representation of the teachings of the non-English reference, though, and reserves the right to demonstrate otherwise.

- All cited references
- References marked by ampersands
- The following:

3. Effect of Information Disclosure Statement (37 CFR 1.97(h))

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the cited information is, or is considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

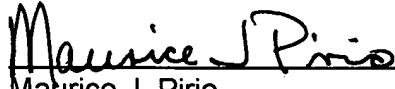
4. Fee Payment

No fees are believed due. However, should the Commissioner determine that fees are due in order for this Information Disclosure Statement to be considered, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 50-0665.

5. Patent Term Adjustment (37 CFR 1.704(d))

The undersigned states that each item of information submitted herewith was cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this statement. 37 C.F.R. § 1.704(d).

Respectfully submitted,
Perkins Coie LLP


Maurice J. Pirio

Registration No. 33,273

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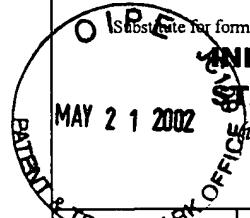
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PTO/SB/08A

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

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Attorney Docket No.

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U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
	AA					
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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

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	AO	Intel Corporation, <i>Pentium™ Processor User's Manual, Volume 3: Architecture and Programming Manual</i> , Mt. Prospect, Illinois, 1993, Chapter 10, "Protected Mode System Architecture Overview", pages 10-1 through 10-12; Chapter 11, "Protected Mode Memory Management", pages 11-1 through 11-25; Chapter 20, "System Management Mode", pages 20-1 through 20-9	
	AP		
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DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).